
TABLE OF CONTENTS

1.0 SCOPE AND PURPOSE.....	1
1.1 SCOPE	1
1.2 PURPOSE	1
2.0 REFERENCED PUBLICATIONS.....	1
3.0 DEFINITION OF TERMS.....	1
4.0 PERSONNEL SAFETY	2
4.1 TRAINING.....	2
4.2 PERSONNEL SAFETY	2
5.0 REQUIRED EQUIPMENT	3
5.1 CDM ESD TESTER.....	3
5.1.1 <i>Current Sensing Element</i>	3
5.1.2 <i>Ground Plane</i>	3
5.1.3 <i>Field Plate/Field Plate Dielectric Layer</i>	4
5.1.4 <i>Charging Resistor</i>	4
5.2 WAVEFORM MEASUREMENT EQUIPMENT.....	4
5.2.1 <i>Cable Assemblies</i>	4
5.2.2 <i>Equipment for High Bandwidth Waveform Measurement</i>	4
5.2.3 <i>Equipment for 1-GHz Waveform Measurement</i>	4
5.3 VERIFICATION MODULES (METAL DISCS).....	4
5.4 CAPACITANCE METER	5
5.5 OHMMETER	5
6.0 PERIODIC TESTER QUALIFICATION, WAVEFORM RECORDS, AND WAVEFORM VERIFICATION REQUIREMENTS	5
6.1 OVERVIEW OF REQUIRED CDM TESTER EVALUATIONS	5
6.2 WAVEFORM CAPTURE HARDWARE.....	5
6.3 WAVEFORM CAPTURE SETUP	5
6.4 WAVEFORM CAPTURE PROCEDURE	5
6.5 CDM TESTER QUALIFICATION/REQUALIFICATION PROCEDURE.....	6
6.5.1 <i>CDM Tester Qualification/Requalification Procedure</i>	6
6.5.2 <i>Conditions Requiring CDM Tester Qualification/Requalification</i>	7
6.5.3 <i>1-GHz Oscilloscope Correlation with High Bandwidth Oscilloscope</i>	7
6.6 CDM TESTER QUARTERLY AND ROUTINE WAVEFORM VERIFICATION PROCEDURE.....	7
6.6.1 <i>Quarterly Waveform Verification Procedure</i>	7
6.6.2 <i>Routine Waveform Verification Procedure</i>	7
6.7 WAVEFORM CHARACTERISTICS	8
6.8 DOCUMENTATION.....	10
6.9 PROCEDURE FOR EVALUATING FULL CDM TESTER CHARGING OF A DEVICE.....	10

7.0 CDM ESD TESTING REQUIREMENTS AND PROCEDURES.....	10
7.1 TESTER AND DEVICE PREPARATION	10
7.2 TEST REQUIREMENTS	11
7.2.1 <i>Test Temperature and Humidity</i>	11
7.2.2 <i>Device Test</i>	11
7.3 TEST PROCEDURES	11
7.3.1 <i>Sample Size</i>	11
7.3.2 <i>Initial Test Voltage</i>	11
7.3.3 <i>Stressing the Pins</i>	12
7.3.4 <i>Field-Induced Charging Method</i>	12
7.3.5 <i>Allowance for Reduced Testing on Large Supply Pin Groups</i>	12
7.4 CDM TEST RECORDING/REPORTING GUIDELINES	12
7.4.1 <i>CDM Test Recording</i>	12
7.4.2 <i>CDM Reporting Guidelines</i>	12
7.5 TESTING OF DEVICES IN SMALL PACKAGES	13
8.0 CDM CLASSIFICATION CRITERIA	13

Annexes

Annex A (Normative): Verification Module (Metal Disc) Specifications and Cleaning Guidelines for Verification Modules and Testers	14
Annex B (Normative): Capacitance Measurement of Verification Modules (Metal Discs)	16
Annex C (Normative): Testing of Small Package Integrated Circuits and Discrete Semiconductors (ICDS)	17
Annex D (Normative): Determination of Withstand Thresholds for Devices to Be Reported with Multiple Thresholds	19
Annex E (Informative): History and Motivation Behind the Creation of a Joint CDM Test Standard.....	21
Annex F (Informative): CDM Tester Electrical Schematic	23
Annex G (Informative): Sample Oscilloscope Setup and Waveform.....	24
Annex H (Informative): Field-Induced CDM Tester Discharge Procedures	27
Annex I (Informative): Waveform Verification Procedures Using Factor/Offset and Software Voltage Adjustment Method	29
Annex J (Informative): Determining the Appropriate Charge Time (Delay) for Full Charging of a Large Module or Device	36
Annex K (Informative): Bibliography	37
Annex L (Informative): Revision History for ANSI/ESDA/JEDEC JS-002	38

Figures

Figure 1: Simplified CDM Tester Hardware Schematic.....	3
Figure 2: CDM Characteristic Positive/Negative Waveform and Parameters.....	9
Figure 3: Verification Module Thickness, Flatness, Dishing, and Doming Illustrations	15
Figure 4: Simplified CDM Tester Electrical Schematic.....	23
Figure 5: Single Discharge Procedure	27
Figure 6: Dual Discharge Procedure	28

Figure 7: Example of a Waveform Verification Flow for Qualification and Quarterly Checks Using the Factor/Offset Adjustment Method	30
Figure 8: Example of a Waveform Verification Flow for the Routine Checks Using the Factor/Offset Adjustment Method	31
Figure 9: Example of the Impact of Factor Adjustment on the Average I_p for the Large Verification Module – High Bandwidth Oscilloscope.....	32
Figure 10: Example of a Waveform Verification Flow for Qualification and Quarterly Checks Using the Software Voltage Adjustment Method.....	33
Figure 11: Example of a Waveform Verification Flow for the Routine Checks Using the Software Voltage Adjustment Method	34
Figure 12: Example Characterization of Charge Time (Delay) Versus I_p	36

Tables

Table 1: CDM Waveform Characteristics for a 1-GHz Bandwidth Oscilloscope.....	8
Table 2: CDM Waveform Characteristics for a High Bandwidth (\geq 6-GHz) Oscilloscope	8
Table 3: CDM ESDS Device Classification Levels.....	13
Table 4: Specification for CDM Tester Verification Modules (Metal Discs).....	14
Table 5: Inclusion of Lower ESD Level High-Speed Pin Data	20
Table 6: Example Waveform Parameter Recording Table for the Factor/Offset Adjustment Method	35
Table 7: Example Waveform Parameter Recording Table for the Software Voltage Adjustment Method	35