

TABLE OF CONTENTS

1.0 SCOPE AND PURPOSE	1
1.1 PURPOSE	1
1.2 SCOPE	1
2.0 DEFINITIONS.....	1
3.0 PERSONNEL SAFETY	1
4.0 TEST ENVIRONMENT.....	1
5.0 DEVICE HANDLING CRITERIA	1
6.0 EQUIPMENT	2
6.1 CALIBRATION	2
6.2 GENERAL TEST EQUIPMENT	2
6.2.1 Oscilloscope	2
6.2.2 Voltage Probe.....	2
6.2.3 Current Probe.....	2
6.2.4 Ammeter	2
6.2.5 Power Supplies.....	3
6.2.6 VDD Reference Supply	3
6.2.7 Test Loads.....	3
6.2.8 Normally Open (NO) Switch.....	3
6.2.9 Pulse Generator: Transient Signal Source.....	3
6.3 TRANSIENT LATCH-UP (TLU) AMPLIFIER.....	4
6.4 DEVICE TEST FIXTURE	4
6.5 SWITCHING MATRIX	5
7.0 TRANSIENT LATCH-UP (TLU) TEST PROCEDURE.....	5
7.1 COMBINED BIAS / STIMULUS TLU OVERVIEW	5
7.2 REQUIRED DEVICE INFORMATION.....	7
7.3 PIN / SIGNAL BIAS INFORMATION.....	7
7.3.1 Multiple V_{DD} pins.....	7
7.3.2 Multiple Supply Sequencing.....	8
7.3.3 Device Biasing: Functional State.....	8
7.3.4 Current Limit Setting.....	9
7.4 TLU TEST EQUIPMENT CONFIGURATION	9
7.4.1 TLU Waveform Monitor	10
7.4.2 TLU Waveform Setting	11
7.4.3 TLU Waveform Verification Procedure.....	11
7.5 DETAILED TEST PROCEDURE.....	15
7.6 DETERMINATION OF FAILURE	17
7.6.1 Absolute Limit.....	17

7.6.2	Relative.....	17
7.6.3	Difference	17
7.6.4	Combined	17
7.7	DETERMINATION OF EOS OR NON-DESTRUCTIVE FAILURE	17
7.7.1	Characterization Before / After testing	18
7.7.2	DC Characterization	18
7.7.3	Functional Testing	18
7.8	FAILURE DOCUMENTATION	18
7.8.1	General Documentation	18
7.8.2	Electrical Test Conditions.....	18
7.8.3	Transient Induced Latch-Up Results.....	19

ANNEXES

Annex A:	General Measurement Procedures.....	20
Annex B:	Alternative Test Methods	23
Annex C:	Standard Data Collection Form	26
Annex D:	Bibliography	29

FIGURES

Figure 1:	Illustration of Device Test Fixtures	5
Figure 2:	Test Setup For TLU (Type 1 Fixture)	6
Figure 3:	Test Setup For TLU (Type 2 Fixture).....	7
Figure 4:	Current Limit Activation Waveform	13
Figure 5:	TLU Pulse Characteristics and Verification Specifications.....	13
Figure 6:	Flowchart Showing the Steps in the Transient Latch-Up Test Procedure.....	14
Figure 7:	Example of Latch-Up Failure Using Combined Bias/Stimulus TLU (Idealized Oscilloscope Graph)	16
Figure 8:	Example of Latch-Up Failure Using Combined Bias/Stimulus TLU (Actual Oscilloscope Plot).....	16
Figure 9:	Example Waveform Illustrating Measurement Locations	20
Figure 10:	Waveform for Determining the Holding Voltage	25