China ESDA Workshop 2019

	Time	Content	Speaker
Thursday, October 24, 2019	9:00 – 9:15	Introduction：ESDA Events and Activites in China	Junjun Li, ESDA
	9:15 – 10:00	Top level ESD protection concepts for low power designs	Harald Gossner, ESDA
	10:00 – 10:45	TLP for ESD Characterization and Quasi-Static ESD Models	Nate Peachey, ESDA
	10:45 - 11:00	Coffee break	
	11:00 – 12:00	ESD Protection design of Advance Circuit	Professor Juin Jei Liou, Zhengzhou University
	12:00- 13:30	Lunch Break	
	13:30 – 14:10	ESD Standard and Its testing method	Ping Lai, 5th Institute, Team Lead
	14:10 – 14:50	ESD modeling and simulation	Professor Yuan Wang, Peking University
	14:50 – 15:30	High Voltage IC ESD protection Design	Hui Wei, TSMC, Manager
	15:30 – 15:40	Coffee break	
	15:40 – 16:20	Interface circuit and its ESD design	Mingliang Li, Huawei
	16:20 – 17:00	High speed Circuit ESD Protection Design	Professor Shurong Dong, Zhejiang University
	17:00 – 17:20	TLP/VFTLP ESD testing and its bias method	Yingjie Gan, ESDEMC
	17:20 - 17:40	IC ESD Failure Analyze Method	Qingguang Zeng, Matak