

This one-of-a-kind industry forum is geared to provide discussion and ways to resolve day-to-day real-world issues. It will also provide discussions concerning standard-driven technical requirements. The forum provides an opportunity to engage with industry experts in an interactive manner featuring parallel session rooms where attendees can move between session rooms as desired. Presentations and discussions are all in parallel with one large group question and answer session. This discussion will feature a special account of how ESD Control work can lead a rise in an organization.

Session 1 Presentation: Specific Application-related Challenges, 8:30 am-9:00 am

Open discussion in room, translation provided 9:00 am - 9:30 am

- Misconceptions around use, qualification, and compliance verification of continuous monitors
- How to assess the suitability of insulative materials and how to determine low-charging properties of materials
- Misuse of ionizers and the importance of evaluating their effectiveness in real-use applications

Session 2 Presentation: Application of Standards, 8:30 am-9:00 am

Open discussion in room, translation provided 9:00 am - 9:30 am

- What is the difference between ESD susceptibility and ESD controls for devices versus assemblies and systems? How to know when to apply which?
- What is the relevance of ANSI/ESD S20.20 on products like flat panel displays, printed circuit board assemblies (PCBAs), or batteries?
- What is the applicability of ANSI/ESD S20.20 for less or more sensitive products than the specified scope?
- Standards are often applied without properly understanding the "purpose" of the requirements. The outcome is a lot of frustrations on both sides of the stakeholders. What are the avenues to address this challenge?

Session 3 Presentation: Risk Assessment, 8:30 am-9:00 am

Open discussion in room, translation provided 9:00 am - 9:30 am

- Controls of conductors, insulators in an EPA; Assessing their impact along the critical paths; more importantly identifying objects that may be excluded from the ESD control program, saving costs!
- CDM risks how to look for them, what to look for and how to address those risks
- How to assess for risks from EOS and what are good mitigation techniques?

Sessions 1, 2, and 3 are offered in parallel.

Session 4: Expert Panel Session: All attendees join all speakers in one discussion room for discussions, 9:30 am-10:30 am

- General Standard Test Method questions
- Grounding requirements in China vs what's stipulated in ANSI/ESD S6.1
- How to find a balance between ESD control requirements and cleanroom contamination control requirements

BREAK 10:30 am - 10:45 am

Session 5: Open discussion session featuring special account of how ESD Control work can lead a rise in an organization, 10:45 am-12:00 noon.

Expert Panel:

Matt Jane, Tesla, Staff Quality Engineer & ESD Program Manager, ESDA Certified Program Manager

Matt Jane is Tesla's Global ESD Control Program Manager who has designed, implemented, and maintains ESD controls in a high-tech electric vehicle manufacturing environment. He is a member of multiple EOS/ESD Association, Inc.'s standards working groups, technical committees, the Board of Directors, and is the Certification Business Unit Manager. Matt is is a member of the United States National Committee / IEC Technical Committee 101, where he represents the United States with international electrostatics standards development.

John Kinnear, IBM Corporation, IBM ESD Coordinator , ESDA Certified Program Manager

John Kinnear is an IBM senior engineer specializing in process & system technology, and facility certification in accordance with ANSI/ESD S20.20. John is well known globally for his technical contributions to national and international standards. He has been the IBM ESD site coordinator for the

Poughkeepsie site since 1989. He is past chairman of the IBM inter-divisional technical liaison committee for ESD protection and is an important member of his company's committee to develop and implement the ESD corporate program for IBM. John has coordinated the testing of large mainframes for compliance to EMC, safety, environmental, shipping, and volatile organic emission standards. He has also been the lead engineer on testing large mainframe systems to EMC emissions and immunity standards for FCC, CE Mark, VCCI, and other national requirements. As a member of EOS/ESD Association, Inc., since 1990, John has served in several standards development committees as well as association management positions. John is the appointed technical adviser to the United States National Committee/IEC technical committee 101, where he represents the United States to the International Electrotechnical Commission (IEC).

Wolfgang Stadler, Intel Germany Services GmbH, Principal Engineer, Intel's ESD Control Program Manager

Wolfgang Stadler joined the semiconductor division of Siemens in 1995, which became Infineon Technologies in 1999. His focus was on development of ESD-protection concepts in CMOS technologies and on innovative ESD topics. In this role he was coordinator of several European and German ESD funding projects. Since 2003 he was also responsible for the measurement characterization of I/O cells and PHYs. In 2011 he joined Intel Mobile Communications which is now Intel Germany Services. Within the Corporate Quality Network of Intel, he is currently responsible for the ESD Control Program of Intel and for ESD risk assessment. He is an active member of the EOS/ESD Association working groups related to device and system testing, ESD control, and ESD process assessment. He is also a member of STDCOM and TAS of the EOS/ESD Association and the German National Committee DKE185 mirroring IEC TC101. Since 2015, he is acting President of the GERMAN ESD FORUM e.V.

Registration

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