



# 3<sup>rd</sup> Annual India ESD Forum

## November 6 & 7, 2023

This is a one-of-a-kind forum in India bringing forth the realities of ESD requirements considering the rapidly advancing semiconductor technologies. This forum is designed to be a two-day online event to bring together key semiconductor companies and esteemed academic institutions in India to learn and exchange information about designing ESD/LU protections for various domains, CDM design validations, and understanding of EMC/EMI concerns. Renowned experts from various industry-leading organizations and academia will be presenting tutorials and invited talks.

### **The tutorials and invited talks will cover the following areas/topics:**

1. Challenges in high-speed I/Os on designer chip, device, and I/O levels
2. Latchup test structures at the foundry level
3. Die-to-die interface
4. State-of-the-art ESD protection and devices
5. HV automotive
6. RF Communications

Back by popular demand! Case study presentations on real-life ESD/LU/EMI/EMC challenges/problems to foster more discussions and knowledgebase expansion. We encourage you to send your case study to [info.eosesda@esda.org](mailto:info.eosesda@esda.org) for an opportunity to discuss it with your peers from industry and academia. Download the submission template from <https://www.esda.org/events/india-esd-forum>.



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**Setting the Global Standards for Static Control!**

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