

Leo G. Henry, Ph.D

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- Pre-amble:** I am an ESD Engineer with a Materials Science background, which includes Electrostatic Discharges (ESD) phenomena; ESD Stress Testing of ICs, ESD Control in the factory work area ; ESD Failure Analysis of ICs . Experienced in implementing programs (using appropriate standards) with procedures for maintaining or reducing or minimizing the negative impact of anomalous ESD.
- Science Education:** Ph.D.. 1985, UC Berkeley, CA, USA, Major: Materials Science & Engineering
M.S. UC Berkeley, CA, USA, Major: Materials Science & Engineering
M.Sc. University of the W.I., Trinidad Major: Physics
B.Sc. University of the W.I., Barbados Major: Physics with Chemistry minor
- ESD Education** 1986-2003. Certificates of completion. Continuous ESD related classes –when
Various institutions. required. --½ day to 2-days per class .
- Teaching Experience: 1971-2009**
- Component and System Level ESD Testing of ICs at Symposiums.
 - Basic TLP for ESD Testing of ICs at the EOS/ESD Symposium, USA.
 - EOS & ESD Testing and FA in ICs at many Symposiums, & Conferences.
 - Physics at the University of the West Indies, Trinidad, Caribbean.
 - Teaching Assistant at UC Berkeley, CA, USA.
 - Materials Science & Failure Analysis Principles at San Jose State Univ, CA.
 - At SEMICON WEST, DISKCON, ESDiscovery and WESCON
- Qualifications:**
- Active participant in an industry-working group (ESDA) for the development of Standards for all aspects of ESD (component and system).
 - Expert knowledge of the industry’s IC level ESD models- HBM, -MM, -CDM, ESD test simulators, TLP testing, & all the ESD test methods/ standards.
 - Proficient knowledge in documents/methods for ESD Control in factories.
 - Proficient knowledge of the system level testing associated with EN-61000-4-2, the IEC standard. Member and active participant of the ESDA WG-14 which developed the system level standard for Calibrating the ESD Gun.
 - Active member of the same ESDA WG-14 , which is now working on a document/procedure for the Cable Discharge Event (CDE).
 - Active member of ESDA WG-5.5, which produced a TLP (Transmission Line Pulse) document for testing ICs.
 - Wrote specifications for ESD Control in the factory for ESD floors, grounding, ESD benches, wrist straps etc etc; and managed a team at AMD to qualify IC products through the reliability lab.
 - As an ESD Program Manager, I teach 3 of the classes at the ESD symposium used by the ESDA to train and test (by examination) in order to grant the PrM (program manager). certificate.
 - As an ESD Engineer, I teach 2 of the classes at the ESD symposium used by

the ESDA to train and test (by examination) in order to grant the Device Design (DD) certificate.

Capabilities	I can work with all design/application teams in conjunction with component packaging teams, product groups, Reliability Labs, ESD/LU Labs and FA Labs to define/update test platforms, address verification/calibration and debug equipment issues, system level ESD test issues. I can actively participate in industry standards to the benefit of your company's needs.	
ESD/TLP Consultant	President, Chief Engineer:	
	Consulting, Training, Testing & Teaching All aspects of ESD. 2002–present	
	Consulted for many semiconductor companies in the areas of ESD Basics, ESD Control, ESD Testing, TLP Equipment sales, TLP Equipment Demo's, TLP Equipment Exhibitor at shows, and as an Expert Witness on ESD and FA issues. Made presentations from technician level to VPs level. This included companies in the USA, Europe and ASIA. Taught ESD classes in all aspects of ESD at these companies, Symposiums, Conferences etc. Partial list of companies: EPI, Round Rock, Texas (2005 to 2007); GTLeader, Santa Clara, CA (2004 to 2006); SQP Products (2003-2005) Fremont, CA., Barth Electronics Inc (2000-2001) Boulder City, NV. Others available if needed.	
ION Systems:	Senior Scientist	2001 – 2002
	<ul style="list-style-type: none"> • Consulted/Analyzed/worked with a team of engineers from several disciplines to solve ESD related problems as they pertain to Ionization. 	
ORYX Corp.	Engineering Manager, ESD Equipment Products	1998 - 1999
	<ul style="list-style-type: none"> • Interacted with company customers, then used the feedback to help the company's project team improve product (ESD Simulators/Testers) development. 	
Advanced Micro Devices (AMD)	Member of the Technical Staff	1983 - 1998
	<ul style="list-style-type: none"> • FA engineer, reliability engineer, ESD engineer, group leader. • Co-authored and published many papers related to ESD. 	
Industry Involvement:	<ul style="list-style-type: none"> • Member of industry ESDA, the ESD Working Group (1990 to present). • ESD Association director: 1995-97; 1998-2000, 2002-2004, 2005-2007. • Silicon Valley EOS/ESD Society- director: 1994-2007. • IDEMA- International Disk drive & Materials Assoc. 1998-2002. • Sr. Member of IEEE, member of ASM, EDFAS. 	
Presentations and Publications:	In the areas of Mat.Sci.,EOS, ESD,& FA. 27 in refereed conference, symposium, proceedings, 12 in magazines & 11 in refereed journals. List available.	
General skills:	Microsoft Office, Internet. Excellent team, verbal, communications, writing and presentation skills. Can also work independently. Self motivated.	
General	Depositions: 3 times in a case associated with the electronics ESD field.	
	Participated in reviewing research documents for one university, and served on a technical team to review the engineering curriculum of a 2-year college.	
References:	Names, phone numbers and email addresses are available upon request.	