




Parisa Iranian

Integrated Circuit(IC) Engineer

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PROFESSIONAL EXPERIENCE

ESD/ LAYOUT ENGINEER, SOFICS (SOLUTIONS FOR ICS)

- Implemented ESD (Electrostatic Discharge) protection solutions for a wide range of integrated circuits and electronic systems, ensuring compliance with industry standards and customer requirements
- Collaborated with IC design engineers to develop ESD protection guidelines and best practice, improving overall ESD robustness of IC designs
- Worked closely with external customer to address their ESD concerns and provide technical support
- Designed and implemented layouts for ESD protection solutions based on design specifications and guidelines on different technology nodes such as TSMC 18 nm and 22 nm
- Performed physical verification checks including DRC and LVS
- Led interns through their internship period at Sofics, providing guidance and support to enhance their understanding of ESD principles and practices

Jun.2022 - Aug.2023
Aalter, Belgium

CAD SUPPORT ENGINEER, SOFICS (SOLUTIONS FOR ICS)

- Assisted with the setup and maintenance of design tools
- Installation and maintenance of different technology nodes
 - Worked on: TSMC 55nm, TSMC 28nm, TSMC 22nm, TSMC 12nm
 - PDK (Physical Design Kit), LVS (Layout Vs Schematic), DRC (Design Rule Check)

Mar.2022 - Aug.2023
Aalter, Belgium

JUNIOR ENGINEER (LAB TECHNISIAN), SOFICS (SOLUTIONS FOR ICS)

- Test and verification of on-chip protection circuits for ESD using different testing tools, such as TLP, VFTLP, 2-pin HBM, LPTLP, system level measurement
 - Conducted data analysis and prepared reports on ESD testing results
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- Contributed to the development of testing procedures and protocols to ensure accurate and reliable ESD measurements
 - Troubleshooting of different setups and lab maintenance
 - Proficiency in operating test equipment (Oscilloscope, multi-meter)

Jun.2021 - Aug.2022

Aalter, Belgium

EDUCATION

VRIJE UNIVERSITY OF BRUSSELS (VUB) – BRUSSELS/ BELGIUM

M.Sc. in Biomedical Engineering (Joint program)

Thesis: Characterization and standardization of a brand-new Fluorescence-lifetime camera (fast-gated CAPS camera), which detects malignant tissues during surgery, through the development of a customized phantom

Sep.2019 - Sep.2021

GENT UNIVERSITY (UGENT) – GENT/ BELGIUM

M.Sc. in Biomedical Engineering (Joint program)

Sep.2019 - Sep.2021

FERDOWSI UNIVERSITY OF MASHHAD (FUM) – MASHHAD/ IRAN

B.SC. in Electrical Engineering

Sep.2014 - Aug.2019

FARHIKHTEGAN HIGH-SCHOOL- MASHHAD/ IRAN

Diploma in Mathematics and Physics

Sep.2010 - Jun.2014

SUMMARY OF SKILLS

- Proficient in ESD testing methodologies, standards, and characterizations techniques
 - Strong understanding of semiconductor physics and device behavior under ESD stress
 - Familiarities with industry standards such as IEC 61000-4-2 and JEDEC
 - Great knowledge of ESD testing tools, building different setups and performing ESD measurements from industrial experience
 - Excellent analytical and problem-solving skills with the ability to conduct failure analysis and root cause investigation
 - Strong communication skills, both written and verbal, with the ability to effectively collaborate with cross-functional teams
 - Creative and problem solving
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- Passionate to match up with new people and international environment
- Passionate to learn more
- Adaptability

CERTIFICATES

EOS/ ESD ASSOCIATION, INC. (ESD/EOS SYMPOSIUM 2022)

- Fundamental of ESD system level
- Basics of ESD and latch-up device physics
- ESD/ EDA verification tools

Sep 2022

Reno, Nevada, United States of America (U.S.)

UDEMY

ESD - An analog design viewpoint

PCB design with Altium designer

SUMMARY OF COMPUTER SKILLS

- Tanner/L-edit
- Altium
- MATLAB
- Linux
- Microsoft office tools
- Latex

LANGUAGES

- English (Advanced)
- French (Beginner)
- Netherlands (Beginner)
- Persian (Native)

REFERENCES

- Koen Verhaege, CEO at Sofics BVBA, Kverhaege@sofics.com
- Olivier Marichal, Chief Engineer at Sofics BVBA, Omarichal@sofics.com
- Maarten Kuijk, Prof. Dr. Ir. at Vrije universiteit Brussels, mkuijk@vub.ac.be
- Hossein Khoshbin Ghomash, Prof. Dr. Ir. at Ferdowsi University of Mashhad (FUM), khoshbin@um.ac.ir